

Prof. P. Chandra Sekhar

M. Tech, Ph.D., SMIEE, LIE, FIETE

I/c Principal
University College of Engineering (Autonomous)
Osmania University, Hyderabad, Telangana, INDIA - 500007



No. 734 /Admn./UCE/OU/2024

Date: 03-04-2024

Advertisement for Project Associate position in the MeitY sponsored Project under Chips to Startup Scheme in the Department of Electronics & CommunicationEngineering, University College of Engineering (Autonomous), Osmania University

Applications are invited from eligible candidates to work as JRF/Project Associate in the MeitY sponsored Project under Chips to Startup (C2S) Scheme titled "The Design, Fabrication and Development of Silicon Proven IP Core for High Resolution AD-PLL" in the Department of Electronics & Communication Engineering, University College of Engineering (Autonomous), Osmania University

Aim of the Project: Aim of the project is to Design, Fabricate and Develop silicon proven, state of art, IP Core for high resolution All Digital Phase Locked Loop (ADPLL) suitable for Multipurpose Low Energy Applications.

For more information regarding the project, visit: http://www.uceou.edu/pdfs/faculty/50022-ADPLL-IP-Core-C2S-MeitTy.pdf

Number of positions available: TWO (2)

Qualification:

- Bachelors degree in Electronics and Communication Engineering/ ME/ M.Tech. in Electronics and Communication Engineering /VLSI / or related area with first class.
- Preference will be given to candidates who are GATE/NET qualified and have experience in Verilog, Verilog-A, Analog IC Design and Cadence Virtuoso.

Fellowship:

- Rs. 35,000 per month + HRA as per norms (for project tenure of maximum four years) for GATE/NET qualified candidates
- Rs. 31,000 per month + HRA as per norms (for project tenure of maximum four years) forother candidates

General Terms and Conditions: The position is temporary and renewable each year subject to satisfactory performance for a maximum of four years. The position is co-terminus with the project. The age limit is 35 years (5 years' relaxation for SC/ST/OBC/ female/Physically Handicapped Candidate).

Completely filled, signed application form must be submitted as per the template provided (Annexure – I). Original documents of age proof/certificates/degrees/mark sheets and other testimonials must be presented at the time of interview. Attested photocopies of these documents will be collected at the time of interview. Only shortlisted candidates will be communicated through e-mail to appear in the interview and no other communications in this regard will be entertained.

No TA/DA will be paid for attending the interview.

Completed applications should reach on or before 15th April, 2024 via email addressed to: chandrasekhar.p@uceou.edu

Prof. P. Chandra Sekhar

I/c Principal University College of Engineering, Osmania University

+91 9866695963, 8331997292



sekharp@osmania.ac.in, sekharpaidimarry@gmail.com



University College of Engineering

(AUTONOMOUS)

OSMANIA UNIVERSITY, HYDERABAD - 500 007



Estd.1929

APPLICATION FORM FOR THE POST OF PROJECT ASSOCIATE IN THE MEITY SPONSORED PROJECT UNDER CHIPS TO STARTUP (C2S) SCHEME IN THE DEPT. OF ELECTRONICS & COMMUNICATION ENGINEERING

Cl No	Qualification	Poord / University	Year of	Percentage		
6.	Educational Qualifications : (From SSC) (add more rows as required)					
5.	GATE/CSIR/UGC Exam : Y / N (If Yes then give the percentage mark/rank and year of passing)					
	Email ID :					
	Mobile :					
4.	Present address for correspondence :					
3.	Date of birth :					
2.	Parent's Name :			Paste a recent passport size photograph		
1.	Name in full (in BLOCK let	ters):				

Sl. No	Qualification	Board / University	Year of passing	Percentage (%)

/. Experience	7.	Experience:
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Name of Post	Name of Organization /Dept.	From	То	Nature of duties performed

8.	Number	of Publications	in	Iournals	/ Conferences ³	* :

Courses/Training in VLSI (provide pro	ot)
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DECLARATION

I hereby, undertake that all the details furnished above are true to the best of my knowledge and I am liable for any disciplinary action, if found otherwise. My candidature may kindly be considered for the above temporary / contractual / short - term project position as per the rules of UGC/DST/MietY.

Place:	
Date:	Signature

INSTRUCTIONS:

- 1. The application form should be neatly typed and sent by e-mail so as to reachbefore the deadline.
- 2. *Enclose photocopies of certificates and reprints of published work.